

S1104SXU30

Flat Base Rectifier Diode

DS6220-1 January 2018 (LN35090)

FEATURES

- Low losses for high efficiency
- Hermetically sealed for long operational life
- Easily mounted down with 4 M8 bolts on 46mm centres
- Available with flying lead, full and half bar connections on request
- Available anode to base and cathode to base
- Selections available for parallel operation

VOLTAGE RATINGS

Part and Ordering Number	Repetitive Peak Voltages V _{RRM} V	Conditions
S1104SXU30 to S1104SXU10	3000 to 1000	$V_{RSM} = V_{RRM} + 100V$

ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

\$1104\$XU28 for a 2800V anode to base device **\$1104\$XD28** for a 2800V cathode to base device

KEY PARAMETERS

 $\begin{array}{ll} V_{RRM} & 3000V \\ I_{F(AV)} & 860A \\ I_{FSM} & 16000A \end{array}$

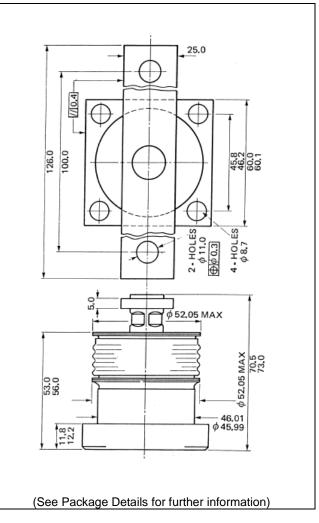


Fig. 1 Package outline

CURRENT RATINGS

T_{case} = 100°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units	
Single Sig	Single Side Cooled (Anode side)				
I _{F(AV)}	Mean forward current	Half wave resistive load	860	А	
I _{F(RMS)}	RMS value	-	1350	А	
I _F	Continuous (direct) on-state current	-	1150	А	

SURGE RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
I _{FSM}	Surge (non-repetitive) on-state current	10ms half sine, $T_{case} = 175$ °C	16	kA
l ² t	I ² t for fusing	$V_R = 50\% V_{RRM} - \frac{1}{4}$ sine	1.28	MA ² s

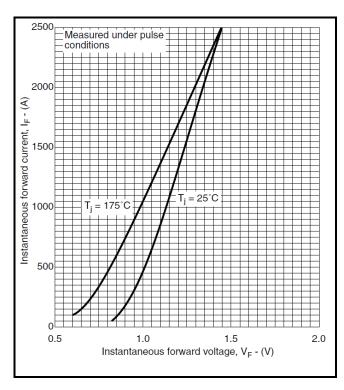
THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions	Min.	Max.	Units
R _{th(j-c)}	Thermal resistance – junction to heatsink	dc	-	0.065	°C/W
		Half wave		0.065	°C/W
		3 phase		0.078	°C/W
T _{vj}	Virtual junction temperature	On-state (conducting)	-	175	°C
		Reverse (blocking)	-	175	°C
T _{stg}	Storage temperature range		-55	175	°C
Torque	Clamping torque		0	22	Nm

CHARACTERISTICS

Symbol	Parameter	Test Conditions	Min.	Max.	Units
V _{FM}	Forward voltage	At 1800A peak, T _{case} = 175°C	-	1.225	V
I _{RM}	Peak reverse current	At V _{DRM} , T _{case} = 1X0°C	-	50	mA
Qs	Total stored charge	I _F = 1000A, dI _{RR} /dt =3A/μs	-	1600	μC
Irr	Peak reverse recovery current	$T_{case} = 175^{\circ}C, V_{R} = 100V$	-	85	Α
V _{TO}	Threshold voltage	At T _{vj} = 175°C	-	0.67	V
r _T	Slope resistance	At T _{vj} = 175°C	-	0.31	mΩ

CURVES



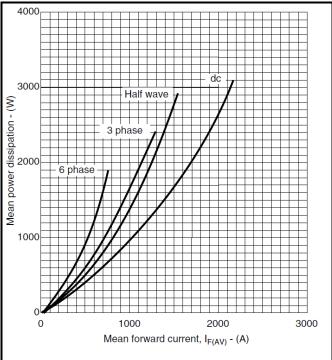
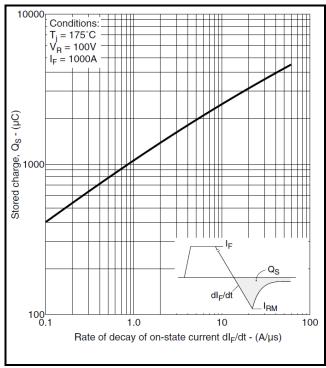


Fig.2 Maximum & minimum on-state characteristics

Fig.3 Dissipation curves

3/7



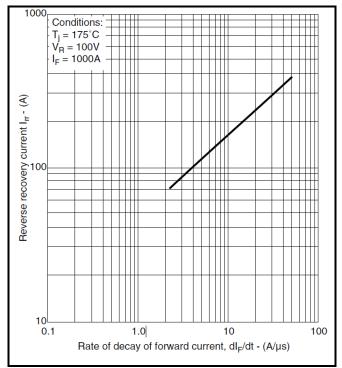


Fig.4 Total stored charge



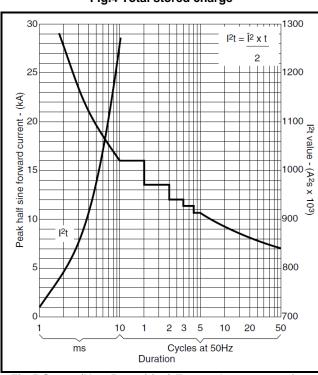


Fig.5 Surge (Non-Repetitive) Forward current vs time

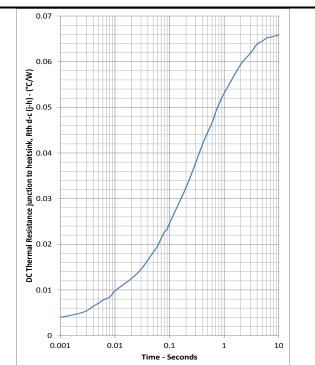
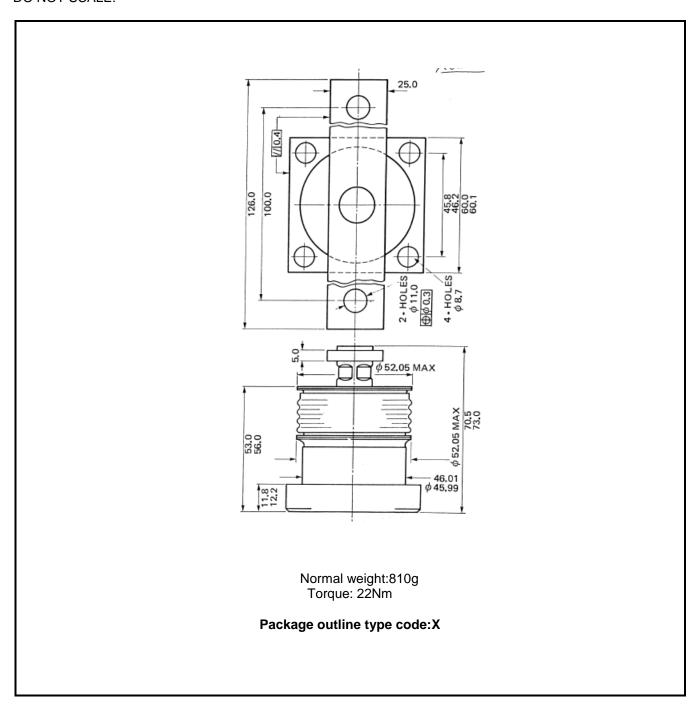


Fig.7 Maximum (limit) transient thermal impedancejunction to heatsink

PACKAGE DETAILS

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



Note:

Some packages may be supplied with gate and or tags.

5/7

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No actual design work on the product has been started.

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The product has been approved for production and unless otherwise notified by Dynex any product ordered will be supplied to the **current version of the data sheet prevailing at the**

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